| L Number | Hits | Search Text | DB | Time stamp |
|----------|-------|---------------------------------------------------------------------------|--------------------|------------------|
| - " | 159 | | USPAT; | 2002/12/13 08:46 |
| | | | US-PGPUB | |
| - | 271 | 712/227.ccls. and @ad<=20000202 | USPAT; | 2002/08/16 11:43 |
| | | | US-PGPUB | |
| - | 336 | 712/215.ccls. and @ad<=20000202 | USPAT; | 2002/08/16 11:44 |
| | | | US-PGPUB | |
| - | 90 | 712/224.ccls. and @ad<=20000202 | USPAT; | 2002/08/16 11:44 |
| | | | US-PGPUB | |
| - | 311 | 712/244.ccls. and @ad<=20000202 | USPAT; | 2002/08/16 11:46 |
| | 610 | | US-PGPUB | 0000400416 11 46 |
| _ | 619 | • | USPAT; | 2002/08/16 11:46 |
| | 2 | @ad<=20000202 exception and (macro same micro) and SSE | US-PGPUB USPAT; | 2002/08/16 12:32 |
| - | 2 | and @ad<=20000202 | US-PGPUB | 2002/08/10 12.32 |
| _ | 542 | SSE and @ad<=20000202 | USPAT; | 2002/08/16 12:32 |
| | Ų 1.2 | | US-PGPUB | |
| _ | 3 | SSE and SIMD and @ad<=20000202 | USPAT; | 2002/08/16 12:35 |
| | | | US-PGPUB | |
| - | 3318 | (micro same macro) and @ad<=20000202 | USPAT; | 2002/08/16 12:35 |
| | | | US-PGPUB | |
| - | 41 | SIMD and (micro same macro) and | USPAT; | 2002/08/16 13:11 |
| | | @ad<=20000202 | US-PGPUB | 2002/02/16 12 12 |
| - | 328 | SIMD and exception and parallel and | USPAT; | 2002/08/16 13:12 |
| _ | 73 | @ad<=20000202 | US-PGPUB USPAT; | 2002/08/16 13:38 |
| - | 13 | SIMD and exception and parallel and ((macro micro) near3 instruction) and | USPAT; US-PGPUB | 2002/00/10 13:38 |
| | | @ad<=20000202 | 03 10105 | |
| _ | 14 | | USPAT; | 2002/08/16 13:42 |
| | 71 | ((macro micro) near3 instruction) and | US-PGPUB | |
| | | (flush cancel) and @ad<=20000202 | | |
| - | 1 | SIMD and exception and parallel and | USPAT; | 2002/08/16 13:49 |
| | | ((macro micro) near3 instruction) and | US-PGPUB | |
| | | (flush cancel) and SSE and @ad<=20000202 | | |
| - | 88 | exception and SSE and @ad<=20000202 | USPAT; | 2002/08/16 13:53 |
| | | 10 | US-PGPUB | 2002/08/16 13:50 |
| - | . 2 | exception and (SSE near10 extension) and @ad<=20000202 | USPAT; US-PGPUB | 2002/08/16 13:50 |
| _ | 5 | exception and (streaming near10 extension) | USPAT; | 2002/08/16 13:54 |
| _ | 3 | and @ad<=20000202 | US-PGPUB | 2002,00,10 13.54 |
| _ | 173 | SIMD | EPO | 2002/08/16 14:23 |
| - | 26 | SIMD and stream | EPO | 2002/08/16 14:25 |
| - | 1998 | (flush cancel) and exception and | USPAT; | 2002/08/16 14:35 |
| | | instruction and @ad<=20000202 | US-PGPUB | 0000/00/15 1: 11 |
| - | 114 | (flush cancel) and exception and SIMD and | USPAT; | 2002/08/16 14:36 |
| | 00 | @ad<=20000202 | US-PGPUB | 2002/08/16 14:44 |
| - | 89 | abdallah.in. | USPAT; US-PGPUB | 2002/00/10 14:44 |
| _ | 11 | abdallah-mohammad.in. | USPAT; | 2002/08/16 14:46 |
| - | 7.1 | abdallan monamma.in. | US-PGPUB | 2502,00,10 11140 |
| _ | 2 | numerical adj exception | USPAT; | 2002/08/16 14:46 |
| | _ | · 5 | US-PGPUB | |
| - | 40 | (macro same micro) and SIMD and parallel | USPAT; | 2002/08/16 15:06 |
| | | and @ad<=20000202 | US-PGPUB | |
| - | 5 | SSE and SIMD | USPAT; | 2002/08/19 11:17 |
| | | | US-PGPUB | 2002/00/10 12:22 |
| - | 80 | denormal | USPAT; | 2002/08/19 13:33 |
| | 9 | denormal game handler | US-PGPUB USPAT; | 2002/08/19 13:33 |
| - | 9 | denormal same handler | US-PGPUB | 2002/00/17 13.33 |
| _ | 38 | ("82" adj1 bit) near2 register | USPAT; | 2002/12/16 09:27 |
| | 56 | \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ | US-PGPUB | |
| - | 3 | MXCSR | USPAT; | 2002/08/20 16:06 |
| | | | US-PGPUB | |
| - | 1 | 6321327.pn. | USPAT; | 2002/08/20 16:24 |
| | | | US-PGPUB | 0000/00/00 15 01 |
| - | 1 | 6357016.pn. | USPAT; | 2002/08/20 16:24 |
| | _ | 5050000 | US-PGPUB | 2002/08/20 16:45 |
| - | 1 | 5859999.pn. | USPAT; | 2002/08/20 10:45 |
| 1 | | | US-PGPUB | <u> </u> |

| - | 1 | 5860017.pn. | USPAT; US-PGPUB | 2002/08/20 16:45 |
|----|-----|--------------------------------------------|--------------------|---------------------------------------|
| _ | 80 | SIMD and macro and micro and (combine | USPAT; | 2002/12/13 08:49 |
| 1 | | merge) and @ad<=20000202 | US-PGPUB | |
| _ | 21 | SIMD and (macro with micro) and (combine | USPAT; | 2002/12/13 09:45 |
| | | merge) and @ad<=20000202 | US-PGPUB | |
| _ | 23 | SIMD and (macro same micro) and (combine | USPAT; | 2002/12/13 13:51 |
| | 23 | merge) and @ad<=20000202 | US-PGPUB | 1 |
| _ | 3 | SIMD and (macro same micro) and (micro | USPAT; | 2002/12/13 10:04 |
| | 3 | with (parallel simultaneous)) and | US-PGPUB | |
| | | exception and @ad<=20000202 | 00 10102 | |
| _ | 1 | | USPAT; | 2002/12/13 10:06 |
| | - | same micro) and @ad<=20000202 | US-PGPUB | 1 |
| _ | 127 | SIMD and (exception and (flush cancel)) | USPAT; | 2002/12/13 10:09 |
| 1 | 12, | and @ad<=20000202 | US-PGPUB | 1 |
| _ | a | SIMD and (exception same (flush cancel)) | USPAT; | 2002/12/13 10:09 |
| 1 | | and @ad<=20000202 | US-PGPUB | ===================================== |
| _ | 3 | exception same (flush cancel) same soon | USPAT; | 2002/12/13 15:29 |
| | | and @ad<=20000202 | US-PGPUB | |
| l_ | 1 | 6338136.pn. | USPAT; | 2002/12/13 15:29 |
| | _ | | US-PGPUB | |
| 1_ | 8 | ((register with "82" with bit) same | USPAT; | 2002/12/16 08:47 |
| | | mantissa) and @ad<=20000202 | US-PGPUB | |
| _ | 0 | (exponent near5 "17" near2 bit) same | USPAT: | 2002/12/16 09:28 |
| | | (double with precision with float) and | US-PGPUB | |
| | | @ad<=20000202 | | j |
| _ | 14 | exception with microinstruction same | USPAT; | 2003/03/25 10:54 |
| | | parallel | US-PGPUB | |
| _ | 110 | exception with parallel same result | USPAT; | 2003/03/25 10:55 |
| | 110 | checpelon with pulation bame robust | US-PGPUB | |
| _ | 5 | exception with (microinstruction | USPAT; | 2003/03/25 11:22 |
| | | micro-instruction) with parallel | US-PGPUB | |
| _ | 88 | (exception trap) with parallel with | USPAT; | 2003/03/27 10:04 |
| | | instruction | US-PGPUB | |
| _ | 1 | (exception trap) same parallel same | USPAT; | 2003/03/27 10:08 |
| | - | (instruction microinstruction) same | US-PGPUB | |
| | | (prevent with (updat\$3 writ\$3)) | | |
| _ | 142 | (exception trap) same parallel same | USPAT; | 2003/03/27 10:13 |
| | | (instruction microinstruction) same | US-PGPUB | |
| | | (updat\$3 writ\$3 cancel) | | |
| _ | 3 | (exception trap) same parallel same | USPAT; | 2003/03/27 10:10 |
| | | (instruction microinstruction) same cancel | US-PGPUB | |
| _ | 86 | (exception trap) same microinstruction | USPAT; | 2003/03/27 14:02 |
| | | same (updat\$3 writ\$3 cancel) | US-PGPUB | |
| - | 0 | execute with all with microinstruction | USPAT; | 2003/03/27 14:03 |
| | | with parallel and macroinstruction | US-PGPUB | |
| - | o | (execute same all same microinstruction | USPAT; | 2003/03/27 14:03 |
| | | same parallel) and macroinstruction | US-PGPUB | |
| - | 1 | (execute same (all every each) same | USPAT; | 2003/03/27 14:04 |
| 1 | | microinstruction same parallel) and | US-PGPUB | V U |
| 1 | | macroinstruction | | |
| - | 10 | (execute same (all every each) same | USPAT; | 2003/03/27 14:05 |
| | | microinstruction same parallel) | US-PGPUB | U |
| - | 5 | (execute with microinstruction with | USPAT; | 2003/03/27 14:13 |
| | 3 | parallel) and macroinstruction | US-PGPUB | |
| - | 0 | (execut\$3 with (all each every) with | USPAT; | 2003/03/27 14:15 |
| | | microinstruction with (parallel | US-PGPUB | |
| | | concurren\$3)) and macroinstruction | | |
| - | 33 | 1 , ======= , = , = | USPAT; | 2003/03/27 14:18 |
| 1 | | (parallel concurren\$3)) and | US-PGPUB | |
| | | macroinstruction | | 1 |
| - | 18 | macroinstruction with two near3 | USPAT; | 2003/03/27 14:21 |
| 1 | | microinstruction | US-PGPUB | 0000/00/00 10 00 |
| - | 3 | "streaming SIMD extensions" | USPAT; | 2003/03/27 17:00 |
| | | | US-PGPUB | <u> </u> |